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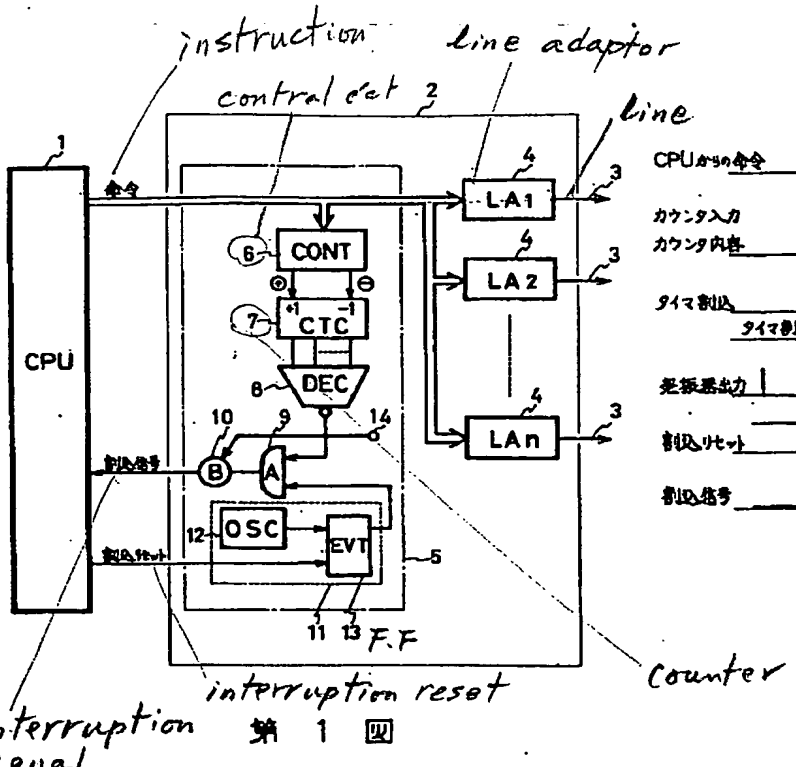
(54) DATA COMMUNICATION PROCESSOR

(57) Abstract:

PURPOSE: The using condition of circuits is always counted by providing a counter to the communication control unit. As a result, the output of timer circuit is stopped when no circuit is in available state. Thus, unnecessary in terruption can be effectively prevented, effectively economizing CPU processing.

In a data communication control device providing a timer circuit sending an interruption signal of a constant period to a central processor unit (CPU), controlling a plurality of lines by instructions of the CPU, the data communication control device includes means for prohibiting the interruption signal from being sent when there is no line in a state of use according to a content of a counter, counting the number of lines which is in a state of use in the counter according to a stop instruction and use of lines of the CPU.



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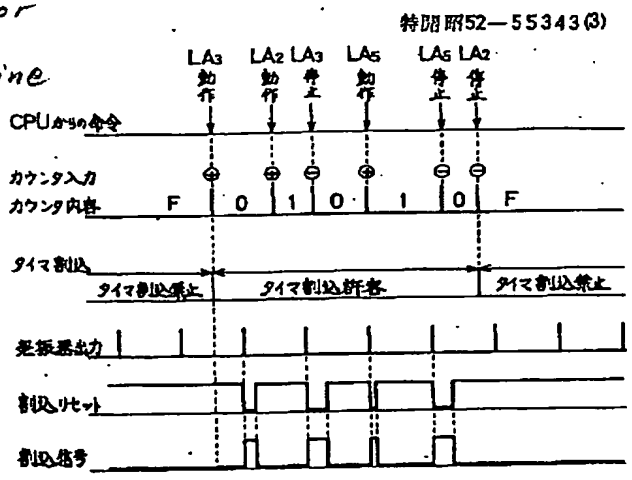

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